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Transmitted herewith for filing under 35 U.S.C. 111 and 37 C.F.R. 1.53 is the patent application of:

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For: **CONTROLLING PROCESSOR-BASED SYSTEMS USING A DIGITAL CAMERA**

Enclosed are:


- ☒ Certificate of Mailing with Express Mail Mailing Label No. **EL515088815US**
- ☒ Five (5) sheets of drawings.
- ☐ A certified copy of a application.
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For	#Filed	#Allowed	#Extra	Rate	Fee
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Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					\$0.00
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Dated: **October 29, 1999**


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APPLICATION

FOR

UNITED STATES LETTERS PATENT

TITLE: CONTROLLING PROCESSOR-BASED SYSTEMS
USING A DIGITAL CAMERA

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Date: October 29, 1999

CONTROLLING PROCESSOR-BASED
SYSTEMS USING A DIGITAL CAMERA

Background

This invention relates generally to processor-based systems and particularly to such systems which have a digital camera coupled to the system.

5 A variety of processor-based systems come with a digital camera or are adaptable to be coupled to a digital camera. Commonly, digital cameras are tethered through an appropriate input port to personal computers. A number of users operate the digital cameras continuously through a
10 tethered connection to the computer.

The camera may be maintained always "on", ready to provide a useful service as the opportunity arises. The camera may be used to implement a video conferencing feature or to stream video over the Internet as examples.

15 A number of techniques are available for controlling processor-based systems. Processor-based systems may receive input commands using a mouse which is tethered to the computer, using an infrared mouse which controls the computer remotely, using a variety of remote control
20 devices, and using keyboards either tethered to the computer or keyboards that are coupled to the computer through infrared or other airwave communications, as a few examples.

In addition, a number of computer systems transition between power consumption states in response to periods of sustained user inactivity. That is, if the keyboard, for example, is not operated for a given time period, the system may transition to a lower power consumption state. With ensuing periods of inactivity, the system may progressively transition to even lower power consumption states. In this way, the amount of power that the system consumes may be controlled. In addition to environmental advantages, the lifetime of the computer may be enhanced by such powering down in response to inactivity.

Sensors may detect the user's presence in proximity to the keyboard. These sensors may be piezoelectric sensors that detect when the user is poised over the keyboard, for example. Again, this type of sensor provides additional information to the computer system to make a judgment about whether or not to transition to lower power consumption modes. If the user is poised over the keyboard about to operate the keyboard, it makes no sense to transition to a lower power consumption mode only to immediately transition back to a higher power consumption mode. Not only is this awkward, but needless transitions waste time and system resources.

Thus, there is a continuing need for better ways to control the power consumption of computer systems.

Summary

In accordance with one aspect, a method of controlling a processor-based system includes receiving video information from a camera. The power consumption state of the system is controlled based on the video information.

Other aspects are set forth in the accompanying detailed description and claims.

Brief Description of the Drawings

Fig. 1 is a front elevational view of a processor-based system in accordance with one embodiment of the present invention;

Fig. 2 is a flow chart for software for implementing one aspect of one embodiment of the present invention;

Fig. 3 is a flow chart for software for implementing another aspect of one embodiment of the present invention;

Fig. 4 is a flow chart for software for implementing another aspect of one embodiment of the present invention;

Fig. 5 is a flow chart for software that implements another aspect of one embodiment of the present invention; and

Fig. 6 is a block diagram for the embodiment shown in Fig. 1.

Detailed Description

A processor-based system 10 shown in Fig. 1, includes a processor-based housing 12, a keyboard 13, a display 14

and a digital camera 16 which may be coupled by a tether (not shown) to the housing 12. As illustrated, the camera 16 is positioned to observe the area in front of the processor-based system 10. Thus, the camera 16 detects the user's presence as well as the lighting conditions proximate to the system 10.

Referring now to Fig. 2, light monitoring software 18 stored on the system 10, is responsible for controlling the power consumption state of the processor-based system 10 in response to ambient light. A timer is reset (block 20) and the keyboard and mouse are checked for their current condition (block 22). At diamond 24, a check determines whether the keyboard and mouse have just been used. If so, the timer is again reset. If not, the check at diamond 26 determines whether the timer has expired. If the timer has expired, indicating a period of inactivity exceeding a predetermined time period, a video frame is grabbed as indicated in block 28. In other words, the camera 16 provides a video frame which may be analyzed at block 30. In particular, the luminance value of the frame may be computed at block 30.

A subsampling of the pixels of a frame may be analyzed, for example, approximately 250 pixels in one embodiment of the invention, evenly distributed throughout the frame. Each pixel's luminance value is computed by converting its red, green, blue (RGB) color space value to a hue, saturation and luminance (HSL) color space value

using well known techniques. Luminance is the brightness portion of a composite video signal. The pixel luminance values are simply summed and divided by the number of samples. The result is the return luminance value.

5 Referring to Fig. 3, the software 42 for determining the return luminance value begins by taking the video frame as described previously (see block 44). The pixel stepping for the pre-set sample set, approximately 250 pixel samples in this example, is computed (block 46). The software
10 steps to the next pixel location (block 48). The pixel at the next location has its RGB color space values converted to an HSL format (blocks 50 and 52). The luminance value of the next pixel (from the HSL space) is added to the accumulated luminance total value, as indicated in block
15 54. If there are more pixels to complete the sample set, the flow iterates. If not, the luminance total is divided by the number of pixel samples as indicated in block 58. The result is then returned to the flow in Fig. 2, as indicated at block 60.

20 Returning to Fig. 2, next the motion value is computed (block 32). As shown in Fig. 4, software 62 for computing the return motion value begins by taking the video frames from the previous steps (Fig. 2) as indicated at block 64. The pixel stepping for a pre-set number of pixels, for
25 example approximately 250 pixels, is computed, as indicated in block 66. The flow steps through each successive pixel as indicated in block 68. At block 70, the pixel at the

new location in the old frame and new frame are accessed. The red value in the old frame is subtracted from the red value in the new frame and this process is repeated for the blue and green values as indicated in block 72. An
5 absolute value of the difference is computed (block 74).

If the result is greater than 50 or some other noise threshold (diamond 76), the result is added to the total motion return value (block 78). At diamond 80 a
10 determination is made as to whether there are additional pixels in the initial set of 250 pixel samples. If not, the motion value is returned (block 82). Otherwise, the flow continues to iterate until all the pixels in the sample have been processed.

Returning again to Fig. 2, at diamond 34 the flow
15 determines whether the returned luminance value differs by more than 40 percent. Of course, 40 percent is merely an exemplary threshold for testing the returned luminance value. If so, a check at diamond 36 determines whether this is the fourth time (or some other number) in a row
20 that the luminance value remained at this level. If so, a check at diamond 38 determines whether motion has occurred during the time period. If not, appropriate changes can be made as indicated in block 40. Among the changes that may be made are to activate a screen saver, implement a power
25 management decision or implement a system utility.

Once the camera 16 is activated, it may capture a frame every second in one embodiment of the invention. The

system 10 computes the luminance value for each frame and compares its value to the luminance value computed for a previous frame. If the number is significantly lower than that of previous frames, the software starts to suspect the lights might be out. It keeps capturing frames, computing both a luminance value and a motion value. If the luminance value continues to be low for several frames and the motion value is also low, the computer screen saver and power management mode may be activated. When full power management is turned on, a keyboard or mouse input signal may be used to reactivate the computer. However, the camera 16 can continue to run with the monitors, printers and hard drives powered down, as long as the processor remains on.

15 The camera continues to capture frames while the computer is in the lower power usage mode or screen saver mode. It compares the frames and computes the luminance value for each frame. If the luminance suddenly increases, the system is immediately returned to full power status and/or the screen saver is deactivated. If the luminance values do not change significantly, and motion occurs for a time period (for example continuously for three seconds), the system may be returned to full power status and/or the screen saver may be deactivated.

25 Turning now to Fig. 5, the software 84 for leaving the screen saver, power management, or system utility modes begins by checking the keyboard and mouse status (block

86). At diamond 88, a check determines whether the keyboard or mouse was just used. If so, the computer is returned to full operational status as indicated in block 102. The timer is reset, as indicated in block 104, and
5 the keyboard and mouse states are checked (block 106).

At diamond 108, a check determines whether the keyboard or mouse were just used. If not, the flow determines (at diamond 110) whether the timer has expired. If so, the screen saver, power management mode or system
10 utilities may be reactivated, as indicated in block 112. Otherwise, the system continues to check the keyboard and mouse state, as indicated in block 106.

If the keyboard and mouse were not used as determined at diamond 88, a video frame is grabbed as shown in block
15 90. A luminance value and motion value are computed as indicated in blocks 92 and 94. If the luminance value does not differ by a preset amount (for example more than 40% at diamond 96), a check at diamond 98 determines whether motion has occurred. If so, a check at diamond 100
20 determines whether motion has occurred two times in a row. If so, the system returns to full activation (block 102). If not, the flow returns to block 86 and checks keyboard and mouse states.

Referring now to Fig. 6, the system 10 may include a
25 processor 114 coupled to an interface 116. The interface 116 may be a chipset or bridge, as two examples. The interface 116 may be coupled system memory 118 and a

display controller 122. The display controller is coupled to the display 14.

The interface 116 may also couple a bus 126. The bus 126 in turn may be coupled through an interface 128 to the camera 16. In addition, the bus 126 may couple an interface 132. The interface 132 may be coupled to a bus 136 and to a storage device such as a hard disk drive 134. The software 18, 42, 62 and 84 may be stored on the hard disk drive 134.

The bus 136 is coupled to conventional components such as a serial input/output device 138. The device 138 couples a mouse 142 and a keyboard 13. The basic input/output system (BIOS) 144 may also be provided on the bus 136.

While the present invention has been described with respect to a limited number of embodiments, those skilled in the art will appreciate numerous modifications and variations therefrom. It is intended that the appended claims cover all such modifications and variations as fall within the true spirit and scope of this present invention.

What is claimed is:

1 1. A method of controlling a processor-based system
2 comprising:
3 receiving video information from a camera;
4 analyzing said information; and
5 controlling the power consumption state of said
6 system based on said video information.

1 2. The method of claim 1 further controlling the
2 operation of system utilities based on said video
3 information.

1 3. The method of claim 1 further controlling the
2 operation of a screen saver based on said information.

1 4. The method of claim 1 wherein analyzing said
2 information includes calculating a luminance value from
3 said video information.

1 5. The method of claim 4 including determining
2 whether said luminance value has changed by a given amount.

1 6. The method of claim 5 including determining
2 whether the luminance value has changed by a given amount
3 for a given time period.

1 7. The method of claim 4 including determining a
2 value indicative of whether the video information indicates
3 motion.

1 8. The method of claim 7 including determining
2 whether said motion has persisted for a given period of
3 time.

1 9. The method of claim 8 including using said value
2 to control the power consumption state of said system.

1 10. An article comprising a medium for storing
2 instructions that cause a processor-based system to:
3 analyze video information; and
4 control the power consumption state of said
5 system based on said video information.

1 11. The article of claim 10 further storing
2 instructions that cause a processor-based system to control
3 the operation of system utilities based on said video
4 information.

1 12. The article of claim 10 further storing
2 instructions that cause a processor-based system to control
3 the operation of a screen saver based on said information.

1 13. The article of claim 10 further storing
2 instructions that cause a processor-based system to
3 calculate a luminance value from said video information and
4 to use said luminance value to control the power
5 consumption state of said system.

1 14. The article of claim 13 further storing
2 instructions that cause a processor-based system to
3 determine whether said luminance value has changed by a
4 given amount.

1 15. The article of claim 14 further storing
2 instructions that cause a processor-based system to
3 determine whether the luminance value has changed by a
4 given amount for a given time period.

1 16. The article of claim 14 further storing
2 instructions that cause a processor-based system to
3 determine a value indicative of whether the video
4 information indicates motion.

1 17. The article of claim 16 further storing
2 instructions that cause a processor-based system to
3 determine whether said motion has persisted for a given
4 period of time.

1 18. The article of claim 17 further storing
2 instructions that cause a processor-based system to use
3 said motion information to control the power consumption
4 state of said system.

1 19. The article of claim 10 further storing
2 instructions that cause a processor-based system to
3 determine whether the lights are on proximate to the
4 system.

1 20. A method of controlling a processor-based system
2 comprising:
3 receiving video information;
4 analyzing said information to develop luminance
5 information; and
6 controlling the operation of software on said
7 system based on said luminance information.

1 21. The method of claim 20 further including
2 controlling the power consumption state of said system
3 based on said luminance information.

1 22. The method of claim 20 further including
2 controlling the operation of a screen saver based on said
3 luminance information.

1 23. The method of claim 20 further including
2 controlling the operation of system utilities based on said
3 luminance information.

1 24. The method of claim 24 including determining
2 whether the video information indicates motion.

1 25. An article comprising a medium for storing
2 instructions that cause a processor-based system to:
3 analyze video information to develop luminance
4 information; and
5 control the operation of software on said system
6 based on said luminance information.

1 26. The article of claim 25 further storing
2 instructions that cause a processor-based system to control
3 one or more of the power consumption state of said system,
4 a screen saver, or system utilities, based on said
5 luminance information.

1 27. The article of claim 26 further storing
2 instructions that cause a processor-based system to
3 determine whether the video information indicates motion.

1 28. A processor-based system comprising:
2 a processor, said processor coupled to a storage
3 device;
4 a digital camera coupled to said processor; and
5 said storage device storing software that
6 controls the power consumption state of said system based
7 on information received from said camera.

1 29. The system of claim 28 wherein said software
2 controls the power consumption state of said system based
3 on information from said camera indicative of motion
4 proximate to said camera.

1 30. The system of claim 28 wherein said software
2 controls the operation of system utilities based on
3 information from said digital camera.

CONTROLLING PROCESSOR-BASED
SYSTEMS USING A DIGITAL CAMERA

Abstract of the Disclosure

5 A processor-based system may be controlled using a digital camera. The digital camera can provide luminance and motion information which may be analyzed to determine whether to alter one or more of the power consumption state, the operation of system utilities, or the operation of a screen saver.

SECRET

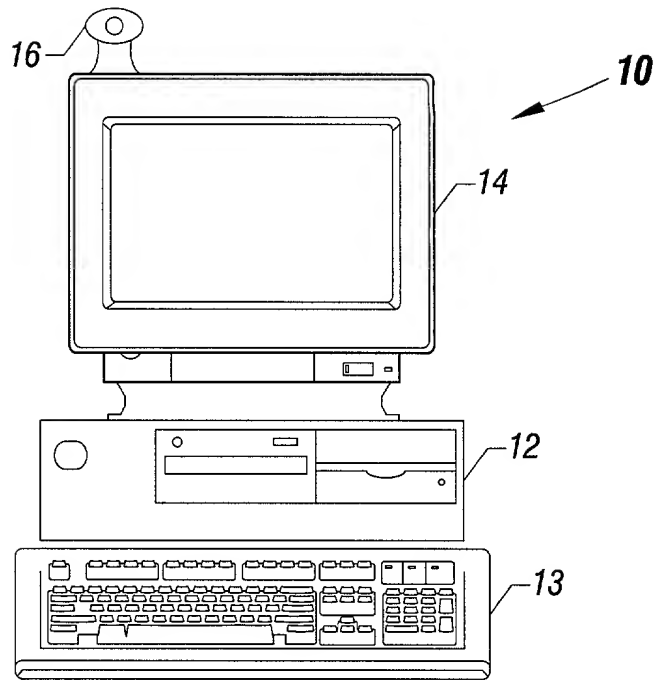


FIG. 1

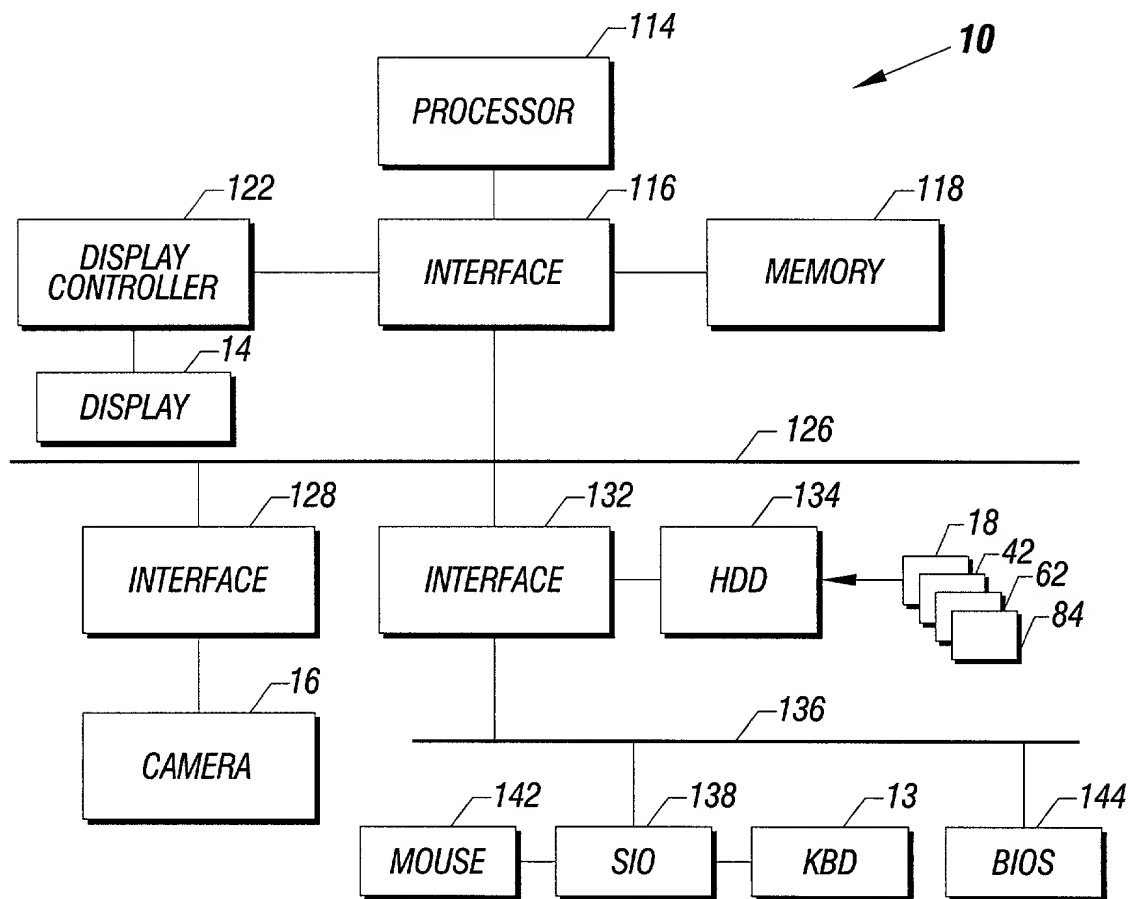


FIG. 6

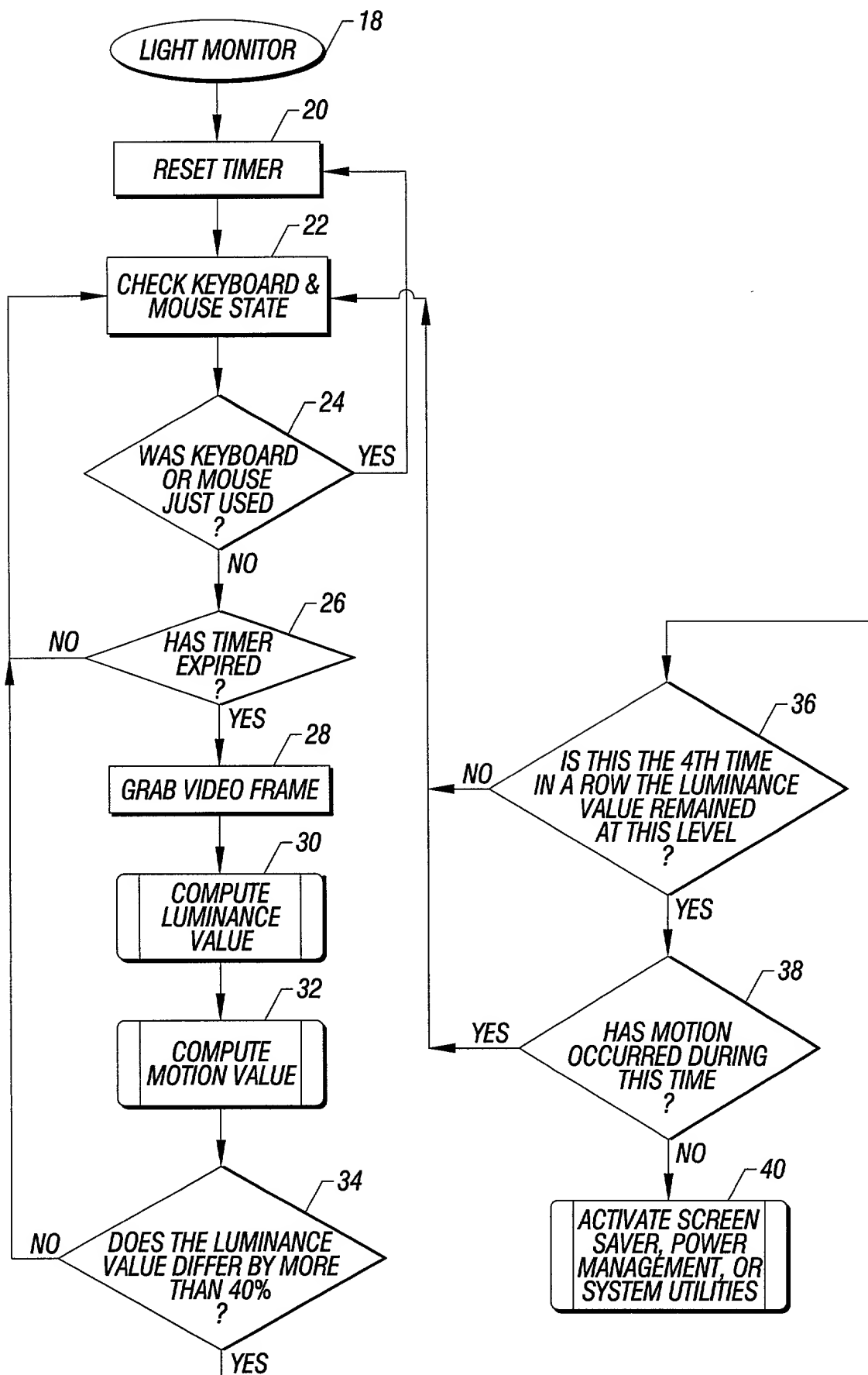


FIG. 2

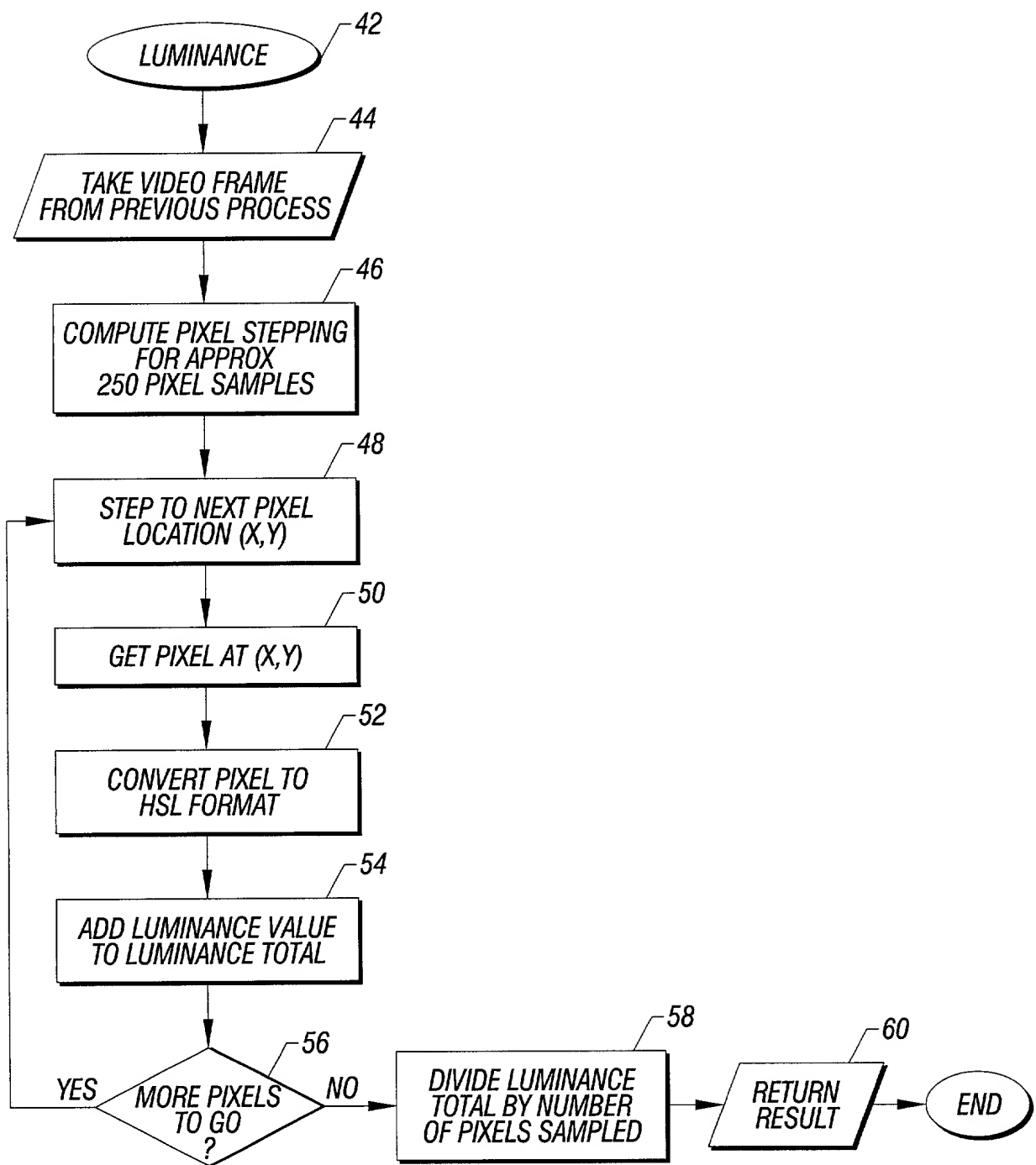


FIG. 3

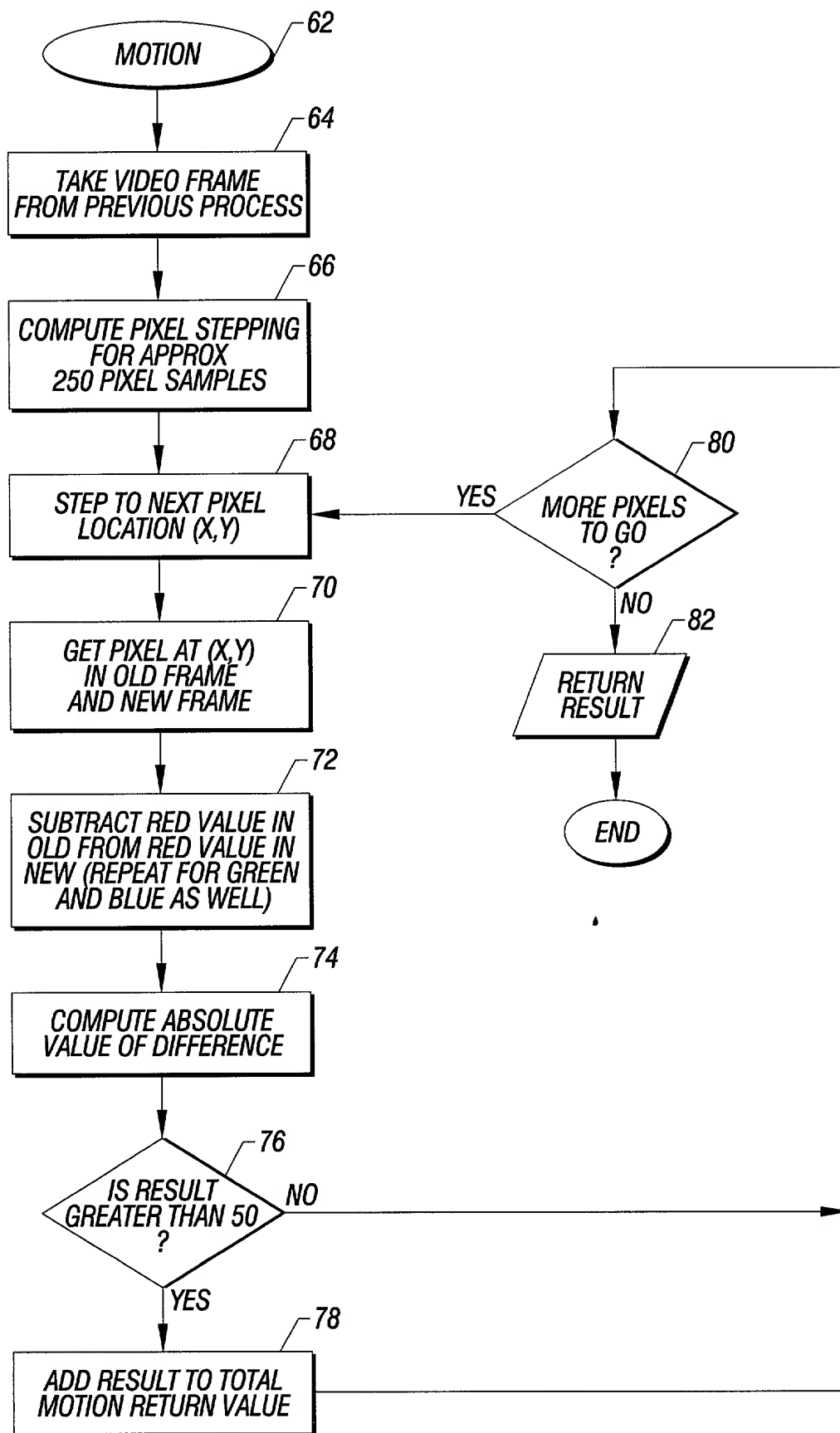


FIG. 4

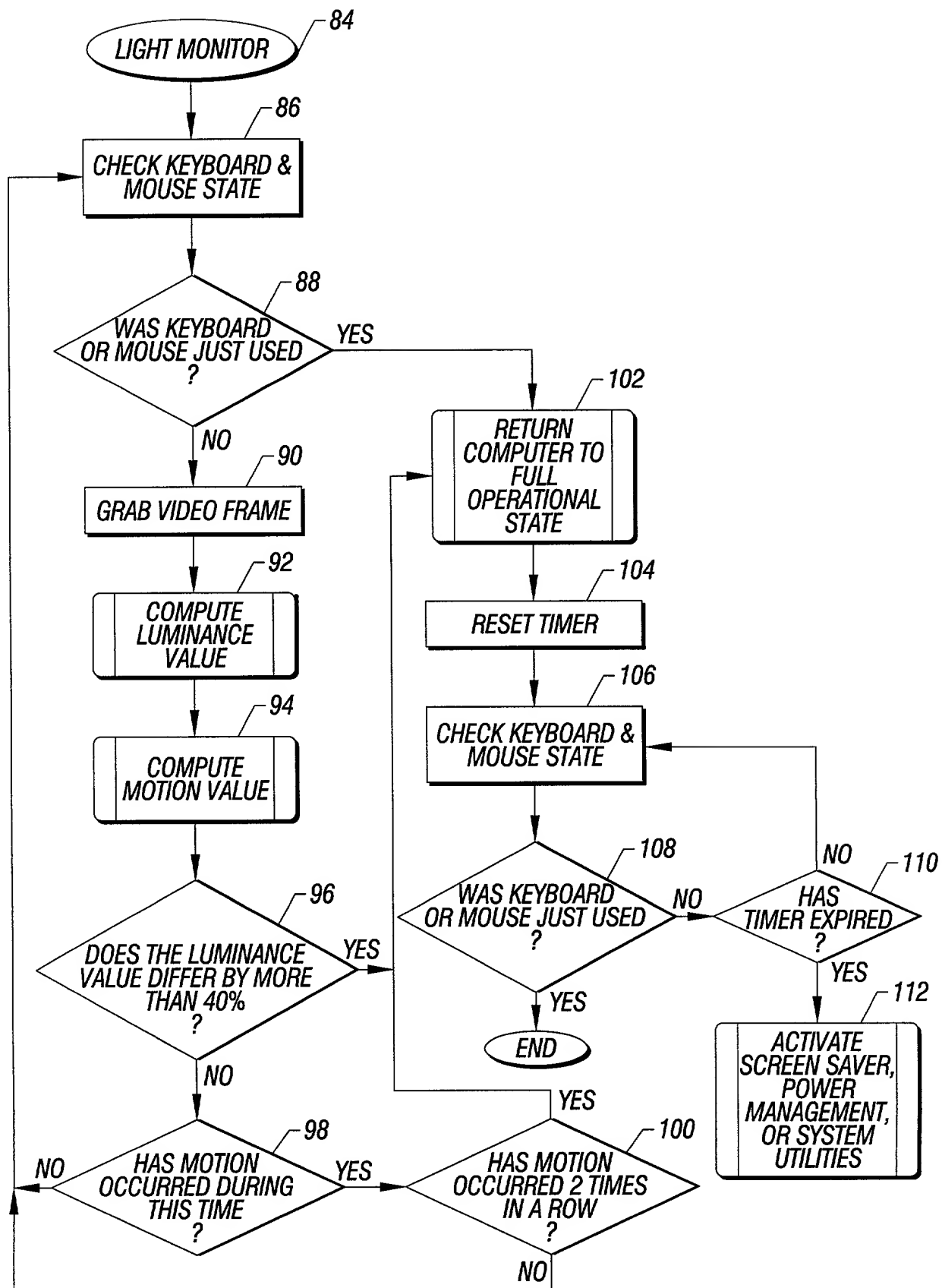


FIG. 5

DECLARATION AND POWER OF ATTORNEY FOR PATENT APPLICATION

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below, next to my name.

I believe I am the original, first, and sole inventor (if only one name is listed below) or an original, first, and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

CONTROLLING PROCESSOR-BASED SYSTEMS USING A DIGITAL CAMERA

the specification of which

✓

is attached hereto.

was filed on _____ as

United States Application Number _____

or PCT International Application Number _____

and was amended on _____

(if applicable)

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claim(s), as amended by any amendment referred to above. I do not know and do not believe that the claimed invention was ever known or used in the United States of America before my invention thereof, or patented or described in any printed publication in any country before my invention thereof or more than one year prior to this application, that the same was not in public use or on sale in the United States of America more than one year prior to this application, and that the invention has not been patented or made the subject of an inventor's certificate Issued before the date of this application in any country foreign to the United States of America on an application filed by me or my legal representatives or assigns more than twelve months (for a utility patent application) or six months (for a design patent application) prior to this application.

I acknowledge the duty to disclose all information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, Section 119(a)-(d), of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Prior Foreign Application(s):			Priority Claimed	
Number	(Country)	(Day/Month/Year Filed)	Yes	No
Number	(Country)	(Day/Month/Year Filed)	Yes	No
Number	(Country)	(Day/Month/Year Filed)	Yes	No

I hereby claim the benefit under title 35, United States Code, Section 119(e) of the United States provisional application(s) listed below:

_____	_____
(Application Number)	(Filing Date)
_____	_____
(Application Number)	(Filing Date)

I hereby claim the benefit under Title 35, United States Code, Section 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, Section 112, I acknowledge the duty to disclose all information known to me to be material to patentability as defined in Title 37, Code of Federal regulations, Section 1.56 which became available between the filing date of the prior application and the national or PCT International filing date of this application:

_____	_____	_____
(Application Number)	Filing Date	(Status-patented, pending, abandoned)
_____	_____	_____
(Application Number)	Filing Date	(Status-patented, pending, abandoned)

I hereby appoint Timothy N. Trop, Reg. No. 28,994; Fred G. Pruner, Jr., Reg. No. 40,779, Dan C. Hu, Reg. No. 40,025; Coe F. Miles, Reg. No. 38,559, and John R. Merklings, Reg. No. 31,716 my patent attorneys, of TROP, PRUNER, HU & MILES, P.C., with offices located at 8554 Katy Freeway, Ste. 100, Houston, TX 77024, telephone (713) 468-8880, and Joseph R. Bond, Reg. No. 36,458; Richard C. Calderwood, Reg. No. 35,468; Sean Fitzgerald, Reg. No. 32,027; David J. Kaplan, Reg. No. 41,105; Leo V. Novakoski, Reg. No. 37,198; Naomi Obinata, Reg. No. 39,320; Thomas C. Reynolds, Reg. No. 32,488; Steven P. Skabrat, Reg. No. 36,279; Howard A. Skaist, Reg. No. 36,008; Steven C. Stewart, Reg. No. 33,555; Raymond J. Werner, Reg. No. 34,752; and Charles K. Young, Reg. No. 39,425; my patent attorneys, of INTEL CORPORATION; with full power of substitution and revocation, to prosecute this application and to transact all business in the Patent and Trademark Office connected herewith.

Send correspondence to Timothy N. Trop, TROP, PRUNER, HU & MILES, P.C., 8554 Katy Freeway, Ste. 100, Houston, TX 77024 and direct telephone calls to Timothy N. Trop, (713) 468-8880.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

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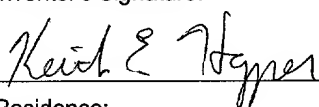
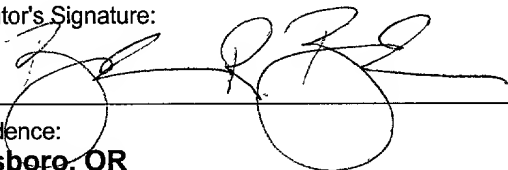
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